

<b>Notice of References Cited</b>	Application/Control No. 09/591,621	Applicant(s)/Patent Under Reexamination GUPTA, VIDYABHUSAN	
	Examiner Herng-der Day	Art Unit 2128	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
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**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Giorgi et al., "An Educational Environment for Program Behavior Analysis and Cache Memory Design", 1997 Frontiers in Education Conference, Proceedings of Teaching and Learning in an Era of Change, 1997, Volume 3, pages 1243-1248.
	V	Coumeri et al., "Memory Modeling for System Synthesis", Proceedings of 1998 International Symposium on Low Power Electronics and Design, August 1998, pages 179-184.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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